



July 5, 2007

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Attn: Art Unit 2811 - Examiner Arora, Ajay

FROM: Stephen B. Ackerman, Reg. No. 37,761  
28 Davis Avenue  
Poughkeepsie, N.Y. 12603

SUBJECT: Serial #: 10/802,566  
File Date: 03/17/2004  
Inventor: Mou-Shiung Lin  
Examiner: Arora, Ajay  
Art Unit: 2811  
Title: A High Performance IC Chip having Discrete Decoupling  
Capacitors Attached to its IC Surface

## RESPONSE TO FINAL OFFICE ACTION

Dear Sir:

In response to the Final Office Action mailed May 4, 2007, please amend the  
above-identified application for patent and consider the remarks as follows:

## CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States  
Postal Service as first class mail in an envelope addressed to: Commissioner of  
Patents P.O. Box 1450, Alexandria, VA 22313-1450 on Jul. 5, 2007.

Signature:   
Stephen B. Ackerman, Reg. No. 37,761

Date: 7/5/07

At  
JFW

1. UET  
Rce  
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with Rcc